

**REMARKS/ARGUMENTS**

Pending claims 1 – 7, 26, 29 and 31 stand rejected under 35 U.S.C. § 103(a) over U.S. Application No. 2003/0140244 (Dahan) in view of U.S. Patent No. 5,895,487 (Boyd). Applicants respectfully traverse the rejection. As to Claim 1, the cited references, either alone or in combination, nowhere teach or suggest an address overload circuit that includes an address multiplexer that is to receive an address from an address translator, nor a data multiplexer coupled to two different sets of memory to output information from one or the other memory set. In this regard, the Office Action concedes that the primary reference, Dahan, nowhere teaches the subject matter. Nor does Boyd. Instead, all that Boyd teaches is that a bank and address selector is controlled to select an address based on a priority determination. There is no teaching or suggestion of providing a translated address to an address multiplexer. Nor is there any teaching or suggestion of a data multiplexer to output information from different memory sets. Instead, the contended data I/O selector of Boyd only provides an output from a single array bank to a selected processor. Accordingly, the rejection of claim 1 and the claims depending therefrom is overcome.

As to claim 26, there is no teaching or suggestion in either Dahan or Boyd for control of both an address multiplexer and a data multiplexer based on an output of a trust co-processor. This is so, at least because the primary reference, Dahan, fails to anywhere teach or suggest that its security state machine 150 is in any way to control address and data multiplexers. Accordingly, claim 26 and the claims depending therefrom are patentable over the proposed combination.

Claim 32 has been amended to include the subject matter of Claims 34 – 36 depending therefrom. As noted in the Office Action, claim 36 was indicated to include allowable subject matter. Office Action, p.11. As none of the cited references anywhere teach or suggest both an address selector and a data selector that are to be controlled by a control signal from a trust coprocessor, it is respectfully submitted that Claim 32 and the claims depending therefrom, including new dependent claims 37 and 38, are patentable.

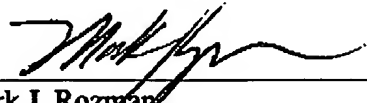
Applicants gratefully acknowledge the indication that claims 9, 11, 13 – 18, 22 and 24 are allowed.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner

is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

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Mark J. Rozman  
Registration No. 42,117  
TROP, PRUNER & HU, P.C.  
1616 S. Voss Rd., Suite 750  
Houston, TX 77057-2631  
(512) 418-9944 [Phone]  
(713) 468-8883 [Fax]  
Customer No.: 21906